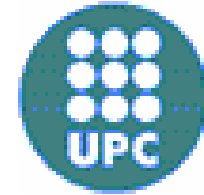




T A R G E T

TOP AMPLIFIER RESEARCH GROUPS
IN A EUROPEAN TEAM



3rd TARGET Summer School

“Linear Power Amplifier Design & Wireless Systems”

24 - 27 July, 2006

Escola Politecnica Superior de Castelldefels (EPSC)
Universitat Politècnica de Catalunya (UPC)

PROGRAM (UPDATED)

Monday, 24	
08:30-09:00	Registration
09:00-09:30	Welcome, Opening
9:30:10:30	Wireless systems: Digital modulations Luis Alonso, <i>UPC</i>
10.30-10:50	COFFEE BREAK
10:50-12:00	Wireless systems: Media access: CDMA, OFDM Luis Alonso, <i>UPC</i>
12.00 – 13.00	TX & RX Block Diagram: Evaluation at system level. PART I Silvia Ruiz, <i>UPC</i>
13:00-14:30	LUNCH
14:30-15:30	TX & RX Block Diagram: Evaluation at system level. PART II Silvia Ruiz, <i>UPC</i>
15:30-15:50	COFFEE BREAK
15:50:17:50	PA Design Fundamentals Markus Mayer, <i>Technische Universität Wien</i> + Simona Donati <i>Politecnico di Torino</i>

Tuesday, 25			
09:00-10:10	High efficient power amplifiers Ingo Dettmann, <i>Universität Stuttgart</i>		
10:10-11.20	Advanced topics in power amplifiers design Paolo Colantonio, <i>Università di Roma "Tor Vergata"</i>		
11.20-11.40	COFFEE BREAK		
11.40-13:00	Circuit Level Linearisation: An Overview Angel Mediavilla - José Angel Garcia <i>Universidad de Cantabria</i>		
13:00-14:30	LUNCH		
14:30:16:00	TUTORIAL ON PA BEHAVIOURAL MODELLING: First part: Theory (PLENARY TALK) J.C..Pedro , <i>IT Aveiro</i>		
16:00:18:00	<table border="1"> <tr> <td>(CONT) TUTORIAL ON PA BEHAVIOURAL MODELLING: (Second part: Practical session: MATLAB) D. Silveira, <i>TU Wien</i> (GROUP A)</td> <td>MODEL EXTRACTION (Practical session: ADS) Maciej Myslinski + Manuel Yarleque <i>Katholieke Universiteit Leuven</i> (GROUP B)</td> </tr> </table>	(CONT) TUTORIAL ON PA BEHAVIOURAL MODELLING: (Second part: Practical session: MATLAB) D. Silveira, <i>TU Wien</i> (GROUP A)	MODEL EXTRACTION (Practical session: ADS) Maciej Myslinski + Manuel Yarleque <i>Katholieke Universiteit Leuven</i> (GROUP B)
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-TBD-	Reception		

Wednesday, 26			
09:00-10:30	<p>System Level Linearisation: Overview</p> <p>Mairtin O'Droma <i>University of Limerick</i></p>		
10.30-10.50	COFFEE BREAK		
10:50-12:00	<p>Digital Predistorters</p> <p>Pere L. Gilibert, <i>UPC</i></p>		
12.00 – 13.00	<p>Feed-Forward linearizers</p> <p>Michael Gadringer <i>Technische Universität Wien</i></p>		
13:00-14:30	LUNCH		
14:30:16:30	<table border="0"> <tr> <td style="vertical-align: top;"> <p>(CONT) TUTORIAL ON PA BEHAVIOURAL MODELLING: (Second part: Practical session: MATLAB) D. Silveira, <i>TU Wien</i></p> <p>(GROUP B)</p> </td> <td style="vertical-align: top;"> <p>MODEL EXTRACTION (Practical session: ADS)</p> <p>Maciej Myslinski + Manuel Yarleque <i>Katholieke Universiteit Leuven</i></p> <p>(GROUP A)</p> </td> </tr> </table>	<p>(CONT) TUTORIAL ON PA BEHAVIOURAL MODELLING: (Second part: Practical session: MATLAB) D. Silveira, <i>TU Wien</i></p> <p>(GROUP B)</p>	<p>MODEL EXTRACTION (Practical session: ADS)</p> <p>Maciej Myslinski + Manuel Yarleque <i>Katholieke Universiteit Leuven</i></p> <p>(GROUP A)</p>
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TIME	Thursday, 27
08:00-10:10	<p>MMIC (PA implementation)</p> <p>Hermann Schumacher <i>Universität Ulm</i></p> <p>+</p> <p>Maurizio Pagani <i>ERICSSON</i></p>
10.10-10.30	COFFEE BREAK
10.30-11:40	<p>DSP-FPGA based linearizers (Implementation issues)</p> <p>Gabriel Montoro, <i>UPC</i></p>
11.40 – 13.00	<p>PA implementation issues (component level)</p> <p>Holger Arthabe. <i>Technische Universität Wien</i></p>
13:00-14:30	LUNCH
14:30:17:00	<p>TUTORIAL ON BASIC PA MEASUREMENTS (Practical session: LABORATORY)</p> <p>Jordi Berenguer <i>UPC</i></p>

SCIENTIFIC ORGANIZATION: Eduard Bertran, UPC

